21. (amended) A method of debugging a processor, said method comprising:

   a) providing information about processor activity in real time; and

   b) associating the instructions executed by the processor with the information about processor activity, wherein said step of providing information about processor activity includes providing information about every instruction executed by the processor.

<center>REMARKS</center>

   The Applicants appreciate the Examiner's careful review of the entire application. In particular, the Applicants appreciate the Examiner's attention to each and every element of every claim.

   The Examiner has objected to the title of the invention as being "imprecise". The Applicants believe that the Title is satisfactory. If the Examiner would be so kind to suggest a better Title, the Applicants will gladly consider changing the Title.

   Claims 1-25 are pending in the application. All claims stand rejected under 35 U.S.C. §103(a) as obvious over Folwell et al.

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